

Fig. 1

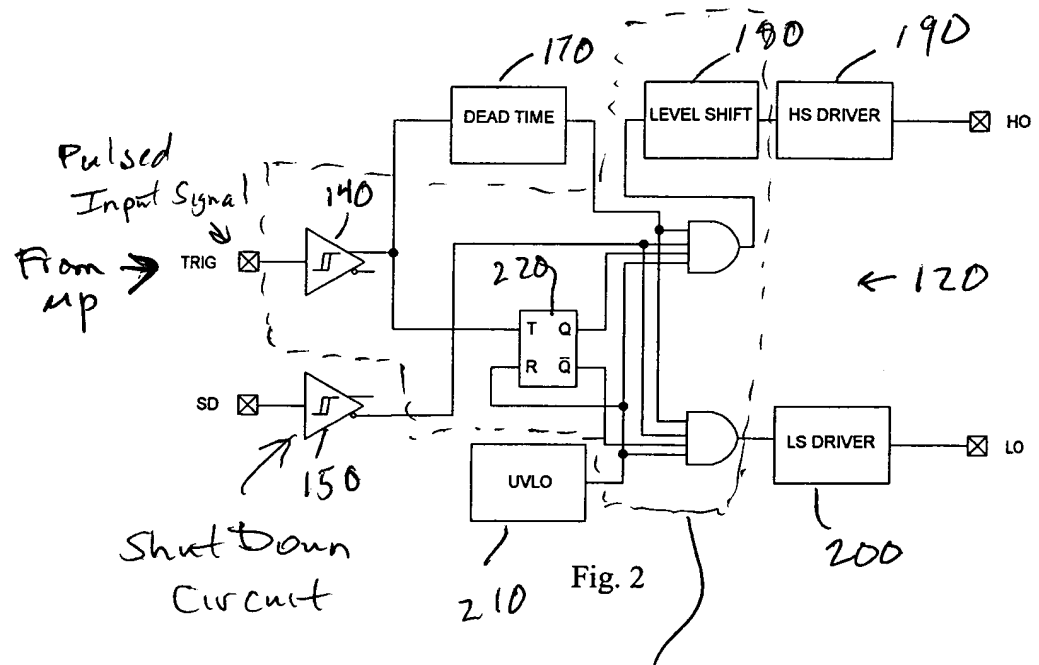


Fig. 2

Input Trigger Circuit